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Sort by [relevance](#) in [expanded form](#)Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)**1** [Operator strength reduction](#)[Keith D. Cooper, L. Taylor Simpson, Christopher A. Vick](#)

September 2001

Transactions on Programming Languages and Systems (TOPLAS), Volume 23 Issue**Publisher:** ACM [Request Permissions](#)Full text available: [Pdf](#) (240.36 KB)**Bibliometrics:** Downloads (6 Weeks): 9, Downloads (12 Months): 102, Downloads (Overall): 945, Citation Count: 5

Operator strength reduction is a technique that improves compiler-generated code by reformulating certain cos computations in terms of less expensive ones. A common case arises in array addressing expressions used in loops. The compiler can replace ...

Keywords: loops, static single assignment form, strength reduction**2** [Exact partitioning of affine dependence algorithms](#)[Jürgen Teich, Lethar Thiele](#)

January 2002

Embedded processor design challenges

Publisher: Springer-Verlag New York, Inc.**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count:

This paper presents a first approach to *partition affine dependence algorithms* (e.g., sets of affine recurrence equations, loop programs with affine data dependences) when mapped onto reduced/fixed size processor array with local interconnect. ...

3 [Addressing thermal nonuniformity in SMT workloads](#)[Jonathan A. Winter, David H. Albonesi](#)

May 2008

Transactions on Architecture and Code Optimization (TACO), Volume 5 Issue 1**Publisher:** ACM [Request Permissions](#)Full text available: [Pdf](#) (1.83 MB)**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 43, Downloads (Overall): 192, Citation Count: 0

We explore DTM techniques within the context of uniform and nonuniform SMT workloads. While DVS is suitable for addressing workloads with uniformly high temperatures, for nonuniform workloads, performance loss occurs because of the slowdown of the cooler ...

Keywords: Simultaneous multithreading, adaptive microarchitectures, clustered microarchitectures, dynamic thermal management, dynamic voltage scaling**4** [An enhanced signalling and routing protocol for ATM extended multistage banyan networks](#)[P. Dananjayan, G. Sivaratne](#)

August 2002


ICCC '02: Proceedings of the 15th international conference on Computer communication**Publisher:** International Council for Computer Communication**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count:

In this paper, an enhanced signalling and routing protocol for ATM extended banyan networks has been proposed. A comparison of the performance measures obtained from the existing and modified protocol simulations indicate that the delays experienced by ...

Keywords: ATM switching, backpressure protocol, banyan networks, multistage interconnection networks packet switching**5** [An adaptive distance computation technique for image retrieval systems](#)[Nuatsawat Hiransakolwong, Kien A. Hua, Sontharee Keomakorn, Khanh Vu, Sheau-Dong Lang](#)

March 2005

SAC '05: Proceedings of the 2005 ACM symposium on Applied computing**Publisher:** ACM [Request Permissions](#)


Full text available:  [Pdf](#) (514.96 KB)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 39, Downloads (Overall): 266, Citation Count: 0

For more than a decade query-by-one-example (QBE) has been a popular query system for content-based image retrieval (CBIR). However, recent research has shown that a single image is not sufficient to form its semantics concept of the intended query. ...


Keywords: content-based image retrieval, dynamic distance metric, query-by-example, range distance

6 [Balancing design options with Sherpa](#)

 [Timothy Sherwood, Mark Oskin, Brad Calder](#)

September 2004 **CASES '04:** Proceedings of the 2004 international conference on Compilers, architecture, and synthesis for embedded systems

Publisher: ACM


Full text available:  [Pdf](#) (292.03 KB)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 44, Downloads (Overall): 274, Citation Count: 4

Application specific processors offer the potential of rapidly designed logic specifically constructed to meet the performance and area demands of the task at hand. Recently, there have been several major projects that attempt to automate the process ...

Keywords: application specific processor (ASIP), area minimization, computer architecture, design space exploration, peicewise linear model


7 [Real-time estimation of the parameters of long-range dependence](#)

 [Matthew Roughan, Darryl Veitch, Patricia Abry](#)

August 2000

IEEE/ ACM Transactions on Networking (TON), Volume 8 Issue 4


Publisher: IEEE Press

Full text available:  [Pdf](#) (237.43 KB)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 27, Downloads (Overall): 533, Citation Count: 7

Keywords: Hurst parameter, estimation, fractal, long-range dependence, on-line, real-time, self-similar, traffic modeling, wavelets


8 [Implicit array bounds checking on 64-bit architectures](#)

 [Chris Bentley, Scott A. Watterson, David K. Lowenthal, Barry Rountree](#)

December 2006

Transactions on Architecture and Code Optimization (TACO), Volume 3 Issue 4

Publisher: ACM 


Full text available:  [Pdf](#) (548.20 KB)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 38, Downloads (Overall): 421, Citation Count: 0

Several programming languages guarantee that array subscripts are checked to ensure they are within the bounds of the array. While this guarantee improves the correctness and security of array-based code, it adds overhead array references. This has ...

Keywords: 64-bit architectures, Array-bounds checking, virtual memory


9 [Order preserving encryption for numeric data](#)

 [Rakesh Agrawal, Jerry Kiernan, Ramakrishnan Srikant, Yirong Xu](#)

June 2004

SIGMOD '04: Proceedings of the 2004 ACM SIGMOD international conference on Management of data


Publisher: ACM 

Full text available:  [Pdf](#) (188.60 KB)

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 226, Downloads (Overall): 1719, Citation Count: 0

Encryption is a well established technology for protecting sensitive data. However, once encrypted, data can no longer be easily queried aside from exact matches. We present an order-preserving encryption scheme for numeric data that allows any comparison ...


10 [Testing challenges for extending SQL server's query processor: a case study](#)

 [Torsten Grabs, Steve Herbert, Xin \(Shin\) Zhang](#)

June 2008

DBTest '08: Proceedings of the 1st international workshop on Testing database systems

Publisher: ACM 

Full text available:  [Pdf](#) (1.25 MB)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 84, Downloads (Overall): 229, Citation Count: 0

With enterprise-class database systems like Microsoft SQL Server, changing mission-critical components such as the relational database engine pose significant challenges to the engineering teams involved. This is because customers carefully configure, ...


11 [Algorithm-Based Fault Tolerant Synthesis for Linear Operations](#)

[Jan-Lung Sung](#), [G. Robert Redinbo](#)

April 1996

IEEE Transactions on Computers, Volume 45 Issue 4

Publisher: IEEE Computer Society


Full text available:  [Publisher Site](#)

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count:

Abstract: High-level synthesis is becoming more important in practical design environments to meet new system requirements and, increasingly, fault tolerance is one especially because of high-speed and low power demands. This paper explores several basic ...


Keywords: 1-fault detectable (1-FD) system, algorithm-based fault tolerant (ABFT) synthesis, data flow graph (DFG), fast Fourier transform (FFT), gain matrix and error space.

12 [Custom feedback control: enabling truly scalable on-chip power management for MPSoCs](#)

 [Siddhant Garg](#), [Diana Marculescu](#), [Radu Marculescu](#)

August 2010 **ISLPED '10: Proceedings of the 16th ACM/IEEE international symposium on Low power electronics and design**

Publisher: ACM 

Full text available:  [Pdf](#) (826.62 KB)

Bibliometrics: Downloads (6 Weeks): 18, Downloads (12 Months): 43, Downloads (Overall): 43, Citation Count: 0

In this paper, we propose Custom Feedback Control, a new dynamic voltage and frequency control architecture for MP-SoC designs that bridges the gap between the two extreme points on the performance versus implementation cost trade-off curve, i.e., fully-centralized ...

Keywords: distributed control, dynamic voltage/frequency scaling

13 [Novel dynamic delay allocation adjustment for improving bandwidth efficiency](#)

[Yuefei Dong](#), [Xiaoliang Liu](#)

March 2010

Computer Communications, Volume 33 Issue 4


Publisher: Butterworth-Heinemann

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count:

End-to-end delay guarantees are critical to many delay-sensitive applications. To ensure such a guarantee on a flow path, the existing approaches usually statically divide its end-to-end delay requirement into per-hop delay requirements. However, such ...


Keywords: Delay allocation, End-to-end delay guarantee, Resource management, Slack

14 [Exploiting underrepresented query aspects for automatic query expansion](#)

 [Daniel Wayne Chaffin](#), [Peter Andreas](#), [Xiaoying Gao](#)

August 2007 **KDD '07: Proceedings of the 13th ACM SIGKDD international conference on Knowledge discovery and data mining**

Publisher: ACM 

Full text available:  [Pdf](#) (842.72 KB)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 105, Downloads (Overall): 767, Citation Count: 5

Users attempt to express their search goals through web search queries. When a search goal has multiple components or aspects, documents that represent all the aspects are likely to be more relevant than those that only represent some aspects. Current ...

Keywords: aspect coverage, global document analysis, query expansion, web search

15 [Face Detection on Embedded Systems](#)

[Abbas Sigdel](#), [Colin Sim](#), [Morteza Biglari-Abhari](#), [Brian C. Lovell](#)

May 2007

ICISS '07: Proceedings of the 3rd international conference on Embedded Software and System

Publisher: Springer-Verlag

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count:

Over recent years automated face detection and recognition (FDR) have gained significant attention from the commercial and research sectors. This paper presents an embedded face detection solution aimed at addressing the real-time image processing requirements ...

16 [Register file partitioning and recompilation for register file power reduction](#)[Xuan.Guan, Yunxi.Fei](#)

May 2010

Transactions on Design Automation of Electronic Systems (TODAES), Volume 15 Issue 3**Publisher:** ACM [Request Permissions](#)Full text available: [Pdf](#) (1.76 MB)**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 51, Downloads (Overall): 51, Citation Count: 0

Register files in modern embedded processors contribute a substantial budget in the energy consumption due to their large switching capacitance and long working time. For some embedded processors, on average 25% of registers account for 83% of the energy consumption.

Keywords: Low-power design, compilers, processor architectures, register file partitioning

17 [Novel approaches to parallel H.264 decoder on symmetric multicore systems](#)[Kue-Hwan.Sihn, Hyunki.Baik, Jong-Iae.Kim, Sehyun.Bae, Hye-Jung.Song](#)

April 2009

ICASSP '09: Proceedings of the 2009 IEEE International Conference on Acoustics, Speech and Signal Processing**Publisher:** IEEE Computer SocietyFull text available: [Publisher Site](#)**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0

Novel approaches to parallel H.264 decoder for symmetric multicore processors are presented. The basic partitioning of the decoder is coarse-grained and hybrid method of the data partitioning and functional partitioning. We investigate the performance of the proposed methods.

18 [Replica synchronisation in grid databases](#)[Sushant.Goel, Hema.Sharda, David.Tanaka](#)

August 2005

International Journal of Web and Grid Services, Volume 1 Issue 1**Publisher:** Inderscience Publishers**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0

Grid architecture is heterogeneous in nature. Replica synchronisation protocols, normally used in homogeneous systems, are not applicable in heterogeneous architectures. In this paper, a replica synchronisation protocol for heterogeneous Grid architecture is proposed.

Keywords: autonomy, consistency, data grid, data management, distributed systems, grid computing, grid databases, grid services, heterogeneity, multiple network partitioning, quorum-based systems, replica synchronisation protocols

19 [References](#)[Wolfgang.Müller, Wolfgang.Rosenstiel, Jürgen.Reif](#)

January 2003

SystemC

Publisher: Kluwer Academic Publishers**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0**20** [Partitioning and mapping of large FEM-graphs by self-organization](#)[M.Dormanns, Hans-Ulrich.Haase](#)

January 1995

PDP '95: Proceedings of the 3rd Euromicro Workshop on Parallel and Distributed Processing**Publisher:** IEEE Computer SocietyFull text available: [Publisher Site](#)**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0

We consider the problem of mapping large scale FEM graphs to highly parallel distributed memory computers. Typically, these programs show a low-dimensional grid-like communication structure. We argue that conventional domain decomposition methods that do not take into account the communication structure are suboptimal.

Keywords: FEM-graph mapping, FEM-graph partitioning, Kohonen neural networks, communication overhead, discretization points, distributed memory systems, domain decomposition methods, finite element analysis, finite element method, graph theory, grid-like structured network, highly parallel computers, highly parallel distributed memory computers, input space, interconnection structure, low-dimensional grid-like communication structure, mapping heuristic, neighbourhood preserving mapping, parallel algorithms, parallel architectures, processor allocation, resource allocation, self-organising feature maps, self-organization, topological similarities

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